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IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re the Application of

TI-30482

Gary L. Swoboda

Art Unit: 2128

Serial No.: 09/943,595

Examiner: Akash Saxena

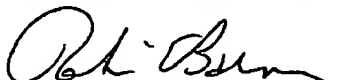
Filed: August 30, 2001

Conf. No.: 2423

For: Collecting and Exporting On-Chip Data Processor Trace and Timing Information With
Differing Collection and Export Formats

CERTIFICATION OF FACSIMILE TRANSMISSION

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Robin E. Barnum

June 14, 2005
Date

FACSIMILE COVER SHEET

<input checked="" type="checkbox"/> FACSIMILE COVER SHEET	<input checked="" type="checkbox"/> AMENDMENT <u>111</u> (23 Pages)
<input type="checkbox"/> NEW APPLICATION	<input type="checkbox"/> EOT <u> </u> (# Page)
<input checked="" type="checkbox"/> DECLARATION (2# Pages)	<input type="checkbox"/> NOTICE OF APPEAL (# Pages)
<input type="checkbox"/> ASSIGNMENT (# Pages)	<input type="checkbox"/> APPEAL <u> </u> (# Pages)
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<input type="checkbox"/> CONTINUATION APP'N (# Pages)	
<input type="checkbox"/> DIVISIONAL APP'N	
NAME OF INVENTOR(S): Gary L. Swoboda	
RECEIPT DATE & SERIAL NO.: Serial No.: 09/943,595 Filing Date: August 30, 2001	
TITLE OF INVENTION: Collecting and Exporting On-Chip Data Processor Trace and Timing Information With Differing Collection and Export Formats	
TI FILE NO.: TI-30482	DEPOSIT ACCT. NO.: 20-0668
FAXED: 6/14/05 DUE: 6/14/05 ATTY/SEC'Y:	RDM/reb

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Texas Instruments Incorporated
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JUN 14 2005

Applicant: Swoboda

Art Unit: 2128

Serial No.: 09/943,595

Examiner: Akash Saxena

Filed: August 39, 2001

Docket: TI-30482

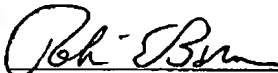
For: COLLECTING AND EXPORTING ON-CHIP DATA PROCESSOR TRACE AND
TIMING INFORMATION WITH DIFFERING COLLECTION AND EXPORT
FORMATS

AMENDMENT UNDER 37 C.F.R. 1.111

Commissioner for Patents
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Robin E. Barnum

Dear Sir:

The following amendments and remarks are offered in response to the Examiner's Office Action dated March 14, 2005. These amendments are respectfully submitted as a full and complete response to that Action.

Please amend the above referenced application as follows: